Abstract - Different parallel transmission lines can be utilized when there is a power flow from a surplus generation area to a load centre in a meshed power grid. The current distribution in these lines depends on the inverse value of the line impedances. The current path with the lowest impedance will reach its maximum transmissibility considerably earlier than the other parallel paths. It thus determines the maximum transmittable power, although the other parallel paths are not loaded to full capacity. There are different possibilities to influence the current distribution, for example using a phase shifting transformer (PST) or reducing the impedances of the partially loaded lines with series capacitors. Another possibility is a tapped reactor series connected to the overloaded line. The different taps are switched with circuit breakers. The current will be displaced into the other paths by increasing the reactors impedance. This paper presents a simulation model of power transmission with three parallel transmission lines, using PSCAD Software. A tapped reactor is inserted into the line with the least impedance. The simulation is carried out with different steady state load flows, and the respective effect on current distribution caused by varied reactor impedance is investigated. Furthermore, the stress on the components, for example the reactor and the circuit breakers, while changing the reactors impedance, is analyzed.

Keywords - Simulation, Load Displacement, Series tapped reactor, Parallel transmission lines, PSCAD

I. INTRODUCTION

One possibility of equal load distribution in parallel lines is to extend a shorter one electrically. This means, an additional reactor has to be inserted into it. Current will then transfer into lines with smaller impedances. Parallel lines connecting two network stations A and B typically have the same length and parameters. One can imagine a network configuration, where three lines are connecting two stations. While one of them connects A and B directly, the other lines are connected via other stations. This would mean that the indirectly connected lines are longer, depending on spatial arrangement. The described configuration which is explained in the following section is used in this paper. The implementation of the electrical network together with the tapped reactor in PSCAD is presented afterwards. Finally, the simulation results are discussed and the stress on the involved elements is presented.
short-circuited by switches. If one reactor step is bypassed, respectively. All three reactor steps have taps, which can be increased if the reactor is inserted completely. The total transmittable apparent power is then \( S_2 = 3747 \text{MVA} \) compared to \( S_2 = 3377 \text{MVA} \) without reactor. In both cases it was assured that no line was overloaded.

E. Vacuum circuit breaker

A vacuum circuit breaker (VCB) is used as switching element in this investigation because of its ability to switch very often. Available breakers can handle currents up to 2.5kA and voltages up to 36kV. Considering the number of load replacements and the voltage and current requirements, this type of breaker seems to be adequate.

III. SIMULATION WITH PSCAD

The simulation tool used is PSCAD, which is a graphical interface to the EMTDC software.

A. Step Size

PSCAD works with a constant step size while running a simulation. This makes it difficult to investigate the long-term behavior and transient effects in one simulation. Hence different step sizes were used. A maximum step size of 20µs is necessary in order to incorporate the characteristics of the VCB model. The accordingly chosen step-size is mentioned in every section.

B. Simulation model of network configuration

Fig. 4 shows the simulation model created for this investigation. On the left side is the feeding point. It is modeled as an ideal voltage source. On the right side is the load which is represented by a fixed load element. Resistance and reactance of this element are calculated and changed depending on the voltage across them. Both nodes are connected by three parallel lines, whereby the shorter line is the middle one. All lines are modeled as Pi-Sections and the values of lumped elements are given in Table 2.

<table>
<thead>
<tr>
<th>Line</th>
<th>Length in km</th>
<th>R in Ohm</th>
<th>L in mH</th>
<th>C in µF</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1, L3</td>
<td>80</td>
<td>2.07</td>
<td>77.67</td>
<td>1.08</td>
</tr>
<tr>
<td>L2</td>
<td>65</td>
<td>1.68</td>
<td>63.10</td>
<td>0.87</td>
</tr>
</tbody>
</table>

The tapped reactor with three reactor steps is serially connected to line 2. Therefore an element separates the three phases and joins them again around the reactor. Every reactor step is modeled as an inductance with \( L_{\text{step}} = 5 \text{mH} \) and a resistance of \( R_{\text{step}} = 10 \text{mΩ} \). The VCB model is represented by a box labeled with “VS” and variable resistance at each step.
Furthermore, active and reactive power is measured in the feeding node, in the load, in line 1 and in line 2. The power meters need a resistance to measure power which was set to a very small value of \(0.1\mu\Omega\).

### C. Configuration of VCB

The VCB model used in this investigation is from [2] where it was used to simulate a vacuum switch. However it can be used for modeling a VCB with new parameters.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>maximum voltage</td>
<td>70 kV</td>
</tr>
<tr>
<td>Resistance (open)</td>
<td>80e-6 (\Omega)</td>
</tr>
<tr>
<td>Resistance (closed)</td>
<td>1000e6 (\Omega)</td>
</tr>
<tr>
<td>slope of dielectric strength</td>
<td>20 kV/(\mu)s</td>
</tr>
<tr>
<td>slope of dielectric recovery</td>
<td>7 kV/(ms)</td>
</tr>
</tbody>
</table>

The maximum voltage of the considered VCB is taken from data sheet [3]. The arc voltage is set to zero. It is only required if energy conversion in the tube has to be considered. A simulation with very high resolution has to be therefore performed. The chopping current isn’t calculated by the formula given in [2], but set to a constant value of 5A. The Simulation results are then comparable and it represents the worst case scenario, as typical values of VCBs lie in the range of 2A–3A [4]. The value of dielectric recovery slope is estimated.

### IV. SIMULATION RESULTS

Fig. 5 shows the loading of the considered line and the effect of switching all reactor steps into it. It is no longer overloaded if all reactor steps are added. The simulation step size was set to 20\(\mu\)s. The lower curve shows the moment of switching off all three VCBs in order to completely insert the reactor. The middle curve shows the loading of line 1 and line 3.

It should be mentioned, that the process of load replacement takes place much faster than it seems in Fig. 5 due to the power meters which are working with a smoothing time constant of 50ms in order to smooth the measured powers sufficiently.

A detailed switching process of one reactor step into the line is shown in Fig. 6. The VCB across this step has to therefore be opened. In the upper graph, the voltages across VCB and the dielectric recovery of the gap can be seen. The lower graphs show the currents of VCB and of reactor step. As current through VCB falls below the value of chopping current, it is interrupted immediately and a transient recovery voltage (TRV) occurs. Its peak value and frequency of oscillation are dependent on the connected circuit. The TRV in phase r exceeds the dielectric strength of gap at \(t = 1.452s\). This is the reason for the peak on the left side in the upper graph. After restriking, current will go on flowing until next zero crossing. As dielectric recovery increases rapidly with increasing contact distance, this effect doesn’t occur again.

Fig. 7 shows the TRV of phase s in detail. The dashed line indicates the voltage in the same phase some periods later. The slope is \(S = 0.114\) kV/\(\mu\)s which is below the typical dielectric strength slope of VCB (Table 3). The TRV peak value is \(V_{peak} = 10.67\) kV which is 1.8 times larger than the peak value of the voltage across this reactor step. It is within the range of VCB parameter. The step size was in this case set to 4\(\mu\)s. Actually, the oscillation isn’t damped that strongly. This is a result of fixed load element, which calculates value of resistance and inductance dependent on voltage.
A simulation with constant resistance and inductance whose values were set to the same values as calculated by fixed load element at the moment of reactor insertion resulted in a weakly damped oscillation. The peak value however remained the same. In reality resistance of load and reactor are damping this oscillation.

A. Delayed switching of further VCBs

If another VCB is switched immediately after the first one, the voltage across it will superimpose on the voltage across the first one. But this effect depends on delay time. For no delay time, the voltages across the VCB don’t have influence on each other. When delay time is at least so high that the second VCB can’t successfully interrupt current, the resulting voltage peak will overlay to the TRV of the first VCB. This leads to much higher TRV peak values. The upper curve of Fig. 8 shows this effect in second phase of the first VCB. Two further VCBs were switched at the same time shortly after the first one in this case. In the lower graph, voltage across second VCB can be seen, which is similar to voltage across third VCB. The resulting TRV peak value across first VCB is 25.5kV. If only one VCB is switched additionally, peak value is 17.3kV. Peak value of unaffected TRV is 10.7kV. A step size of 4µs was used and delay time for switching further VCBs was set to 2.7ms. If they are switched after first VCB interrupted all currents successfully, the overlaid TRV on voltage across first one has a peak value, which is less than TRV peak in the beginning. This effect looks similar to the oscillation in Vt_step1 of upper graph at time 1.454s.

B. Short-circuit reactor step

A high stress on VCB appears when it short-circuits the reactor step. Dielectric strength decreases with decreasing contact distance. At the moment of ignition-through, voltage across VCB is forced to nearly zero and the reactor current starts to decay with a time constant of \( \tau = \frac{L_{\text{step}}}{R_{\text{step}}} = 0.5s \). This decaying circulating current superimposes on current through VCB, thus causing a current with double amplitude in the worst case scenario. Fig. 9 shows currents of reactor step and of respective VCB. Further investigations are necessary to clarify the possibility of VCB to handle this stress. The resistance of reactor step (10mΩ) has a very small value. Higher values would reduce time constant and therefore reduce time of stress with higher current amplitudes.

C. Short-circuit on load side

It should be pointed out, that in case of short-circuit on the load side, the accurate operation of VCB is in certain circumstances not guaranteed. Line current in this case will reach comparatively high values. If there is a controller, which responds very fast and is going to increase inductance of reactor, VCB isn’t able to interrupt this high current, because TRV peak would always exceed maximum dielectric strength. This leads to high thermal impact and finally to destruction of VCB.

V. CONCLUSIONS

Principally, a taped reactor can force equal load distribution in parallel lines by extending a shorter one electrically. But the VCB which short-circuits the respective step has to handle multiple stresses. This is on the one hand a TRV with higher amplitude, namely when VCB is switched off in order to insert the inductance, and on the other hand very high currents in case of short-circuiting the reactor step. The TRV voltage peak should not overstress VCB if the inductance of one reactor step is well dimensioned. Several contributing parameters should be taken into account such as number of steps of tapped reactor, maximum line current and line parameter. The stress with high currents shortly after short-circuiting a step is more critical. With a higher reactor resistance, time constant of decaying current would be smaller, so that stress with high current amplitudes would be in a range, which VCBs are able to handle. Further investigations are necessary and will be accomplished.

REFERENCES